Unit Design Phase Defects

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| **Review Form** | | | |
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|  | **Document Title:** | Design and Coding Review Specification | |
|  | **Reviewer Name:** | **Szabo Mark, Terebes Liviu** | |
|  | **Review date:** | **15.03.2023** |  |
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| **Crt. No.** | **Checked Item** | **Doc. page/line** | **Comments/ improvements** |
| 1 | D\_07 | 3 point 1 | “Update student” should be FR.1.2 |
| 2 | D\_07 | 3 point 3 | Some use cases which appear in the diagram are missing from the “Functional Requirements” table. |
| 3 | D\_07 | 3 point 3.1 | The precondition is too vague. The System Response actions are not detailed enough. |
| 4 | D\_01 | 5 point 4.2 | The described architecture (relations between classes) is not ideal. Until there is a grade assigned, there is no relation between a student and an assignment (which, logically, should be). |
| 5 | D\_02 | 3 | The report requirements mentioned in the requirement document are not accounted for in the architecture. |
| 6 | D\_01 | 5 point 4.4 | The “System behavior” part is missing a number of details, including diagrams. |
| 7 | D\_06 | 6 | The “Service” class acts as a monolith, thus making it hard to add new changes to the system. |
| 8 | D\_03 | 7 point 5.2 | The error handling is not presented in the sequence diagrams. |
| 9 | D\_02 | 7 | The sequence diagrams do not match with the class diagrams (the class “Controller” is missing, the UI methods are not specified, etc.). |
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|  | **Effort to review document (hours): 1** | |  |